

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of	)	
	)	
Hideto HIDAKA	)	Group Art Unit: Unassigned
	)	
Application No.: Unassigned	)	Examiner: Unassigned
	)	
Filed: July 10, 2003	)	Confirmation No.: Unassigned
	)	
For: MEMORY DEVICE CAPABLE OF	)	
PERFORMING HIGH SPEED	)	
READING WHILE REALIZING	)	
REDUNDANCY REPLACEMENT	)	

**FIRST**  
**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicant hereby submits the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98.

Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

**The documents are discussed in the specification.**

The documents are being submitted within three (3) months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later. Since documents are being filed within the time period set forth in 37 C.F.R. § 1.97(b) no fee or statement is required.

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: July 10, 2003

By: 

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Substitute for forms 1449A/PTO & 1449B/PTO  <b>FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	ATTORNEY'S DKT NO. 009683-471	APPLICATION NO. Unassigned
	APPLICANT Hideto HIDAKA	
	FILING DATE July 10, 2003	GROUP Unassigned

U.S. PATENT DOCUMENTS				
Examiner Initials	Document Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Issue/Publication Date (MM-DD-YYYY)

FOREIGN PATENT DOCUMENTS					
Examiner Initials	Document Number	Kind Code (if known)	Country	Date of Publication (MM-DD-YYYY)	Translation Yes No

NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
*	R. Scheuerlein et al., "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in Each Cell", 2000 IEEE ISSCC Digest of Technical Papers, TA7.2, February 2000, pp. 94-95, 128-129, 409-410.
*	M. Durlam et al., "Nonvolatile RAM based on Magnetic Tunnel Junction Elements", 2000 IEEE ISSCC Digest of Technical Papers, TA 7.3, pp. 96-97, 130-131, 410-411.
*	P.K. Naji et al., "A 256kb 3.0V 1T1MTJ Nonvolatile Magnetoresistive RAM", 2001 IEEE ISSCC Digest of Technical papers, TA7.6, February 2001, pp. 94-95, 122-123, 404-405.

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **SEND TO:** Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

\*The documents are discussed in the specification.